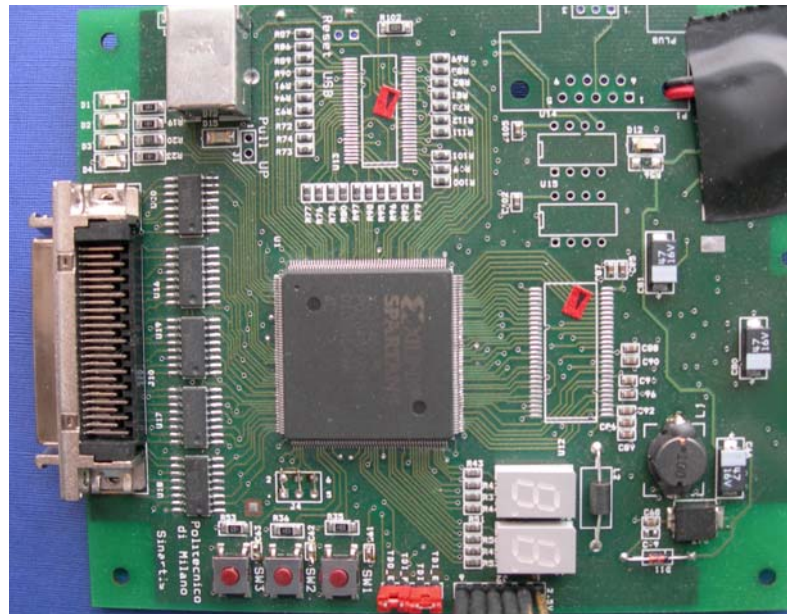


FPGA Based Logic Emulation

Reconfigurable Computing
System & Algorithm Implementation



Video Processing Technologies



Designers can emulate up to 400k system gates on this USB hosted PCB based on the Xilinx Spartan3 – 400 and implement digital systems or signal processing algorithms in a fully configurable processor ready for use. Easy configuration using EEPROM.

Technical Specifications

FPGA: 1xSpartan 3 400 PQ208
Interfaces : 1xUSB (Cypress fx2 56pvc), 1xRS232, 20xLVDS OUT (2.5V - 100ohm),
1xJTAG (FPGA)
Memory : 1x64Mb SDRAM, 1x4Mb Flash XCF04S (for FPGA configuration)
Controls: 3 Microswitch, 2 Display 7 segments
Outputs: 20 LVDS output lines
Power Supply : 12V 300mA

Designers can emulate up to 400k system gates on this USB hosted PCB based on the Xilinx Spartan3 – 400 and implement digital systems or signal processing algorithms in a fully configurable processor ready for use. Easy configuration using EEPROM.